

Third Semester B.E. Degree Examination, Dec. 07 / Jan. 08
Analog Electronic Circuits

Time: 3 hrs.

Max. Marks:100

Note : Answer any FIVE full questions.

- 1 a. What do you understand by 'reverse recovery time' of a diode? Explain. (05 Marks)
 b. The input voltage V_i to the two level clipper circuit [as shown in Fig. Q1(b)] varies linearly from 0 to 150 V. Sketch the output voltage V_o to the time scale. Assume diodes as ideal. *

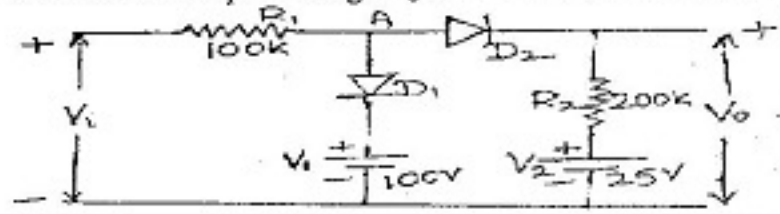


Fig. Q1(b)

(10 Marks)

- c. Draw and explain the working of the clamper circuit which clamps the positive peak of a signal to zero volts. (05 Marks)
 2 a. Explain the circuit of a transistor switch being used as an inverter. (05 Marks)
 b. Derive an expression for the stability factor, $S(I_{CO})$, of the voltage divider Bias circuit. (06 Marks)
 c. In the circuit of Fig. Q2(c) $V_{CC} = 10$ V, $R_C = 1.5$ k Ω , $I_C = 2$ mA, $V_{CE} = 5$ V, $V_{BE} = 0.7$ V, $\beta = 50$ and stability factor $S = 5$, find R_1 and R_2 .



Fig. Q2(c)

(09 Marks)

- 3 a. Derive the expressions for A_V , A_I , Z_i and Z_o for CE fixed bias configuration using complete hybrid equivalent model. (12 Marks)
 b. For common base amplifier shown in Fig Q3(b). determine Z_i , A_i , A_V and Z_o using complete hybrid equivalent model.

Given $h_{ie} = 1.6$ k Ω , $h_{fe} = 110$

(08 Marks)

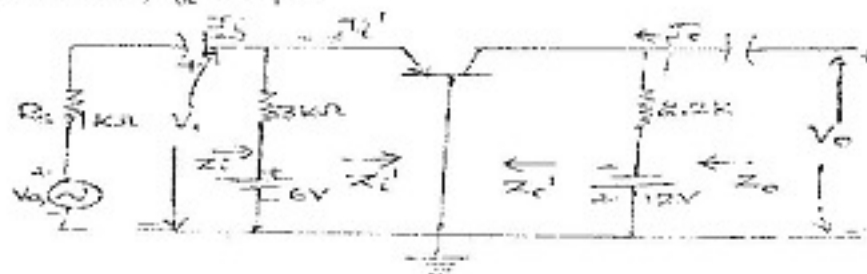
 $h_{re} = 2 \times 10^{-4}$, $h_{oe} = 20$ μ S.

Fig. Q3(b)

- 4 a. The input power to a device is 10,000 W at a voltage of 1000 V. The out put power is 500W and the output impedance is 20 Ω .
 i) Find power gain in decibels, ii) Find voltage gain in decibels. (04 Marks)
 b. Describe miller effect and derive an equation for miller input and output capacitances.
 c. Discuss the factors that affect the low frequency response of a BJT-CE amplifier. (10 Marks)

- 5 a. Fig. Q5(a) shows cascading of an emitter follower circuit and a common base circuit. (09 Marks)
- The loaded gain of each stage
 - The total gain for the system, A_V and A_{V_S} .
 - The total current gain for the system
 - The total gain for the system if the emitter follower circuit were removed.

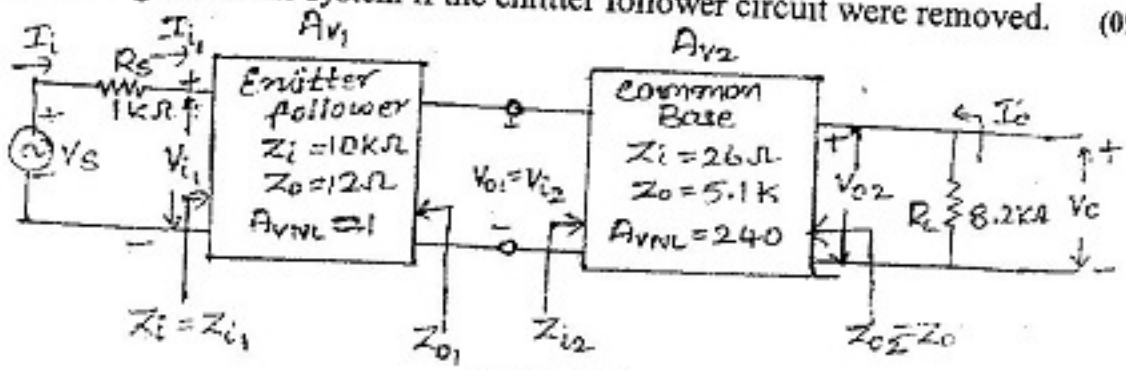


Fig. Q5(a)

- Show that negative feedback increases the bandwidth of an amplifier. (06 Marks)
 - Derive an expression for output resistance of a voltage series feedback amplifier. (05 Marks)
- 6 a. With the help of a circuit diagram, explain the working of class-B pushpull amplifier. Obtain an expression for maximum conversion efficiency of this amplifier. (09 Marks)
- Discuss the different types of power amplifiers. (05 Marks)
 - For distortion readings of $D_2 = 0.15$, $D_3 = 0.01$ and $D_4 = 0.05$ with $I_1 = 3.3$ Amps, $R_C = 4\Omega$. Find - i) Total harmonic distortion D, ii) Fundamental power component, iii) Total power. (06 Marks)
- 7 a. What is Barkhausen criterion? Explain how oscillations start in an oscillator. (07 Marks)
- With the help of a neat circuit diagram, explain transistor colpitts oscillator. Write expression for the frequency of oscillation. (08 Marks)
 - A quartz crystal has $L = 0.12$ H, $C = 0.04$ pF, $C_M =$ pF and $R = 9.2$ kΩ. Find i) Series resonant frequency, ii) Parallel resonant frequency. (05 Marks)
- 8 a. Discuss the differences between FET and BJT. (04 Marks)
- Derive the expressions for Z_i , Z_o and A_V for common drain JFET amplifier. (09 Marks)
 - A dc analysis of source follower network shown in Fig. Q8(c) results in $V_{GSQ} = -2.86$ V and $I_{DQ} = 4.56$ mA. Determine i) g_m , ii) r_d , iii) Z_i , iv) Z_o with and without r_d , v) A_V with and without r_d . $I_{DSS} = 16$ mA, $V_P = -4$ V, $Y_{OS} = 25$ μS. (07 Marks)

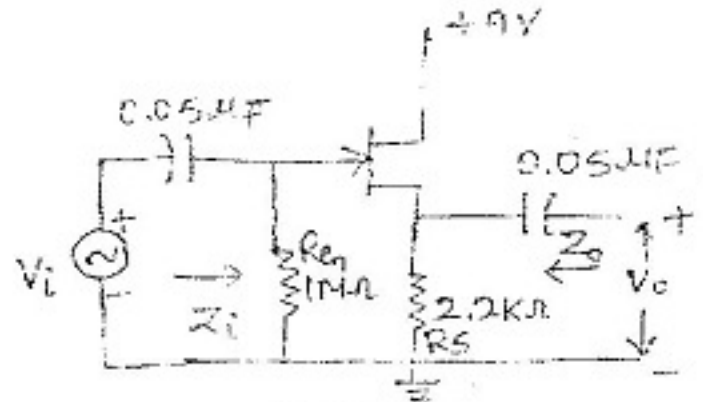


Fig. Q8(c)

(07 Marks)
